

晶采光電科技股份有限公司 ■ AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

AM-800600C4TMQW-TA3H

☐ Approved For Specifications & Sample

AMPIRE CO., LTD.

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Date: 2013/05/13 AMPIRE CO., LTD.

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RECORD OF REVISION

Revision Date	Page	Contents	Editor
2013/05/13		New Release	Bob

1. Features

8 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 8" TFT-LCD panel, LED backlight, LED driver unit ,Resistive Touch Panel and power circuit unit.

(1) Construction: 8" a-Si TFT active matrix, White LED Backlight and LED driver unit.

(2) Resolution (pixel): 800(R.G.B) X600

(3) Number of the Colors: 262K colors (R, G, B 6 bit digital each)

(4) LCD type: Transmissive, normally White

(5) Interface: LVDS interface 20 pin

(6) Power Supply Voltage: 3.3V for logic voltage, 5.0V for LED driver power voltage.

(7) Viewing Direction: 6 O'clock (Gray Inversion)

(8) Resistive Touch Panel

2. PHYSICAL SPECIFICATIONS

Item	Specifications	unit
LCD size	8 inch (Diagonal)	
Resolution	800 x 3(RGB) x 600	dot
Dot pitch	0.0675(W) x 0.2025(H)	mm
Active area	162.0(W) x 121.5(H)	mm
Module size	183.0(W) x 141.0(H) x 9.92(D)	mm
Surface treatment	Anti-Glare	
Color arrangement	RGB-stripe	
interface	LVDS	
Weight	T.B.D.	g

3. ABSOLUTE MAX. RATINGS

Item	Symbol	Valu	ues	UNIT	Note
item	Symbol Min.		Max.	UNII	NOTE
Power voltage	VDD	-0.3	4	V	
1 Ower voilage	VLED	-0.3	6.0	v	
Operation temperature	TOP	-20	70	$^{\circ}\!\mathbb{C}$	
Storage temperature	Tst	-30	80	$^{\circ}\!\mathbb{C}$	
LED Reverse Voltage	VR	-	1.2	V	each LED Note 2
LED Forward Current	IF	-	25	mA	each LED

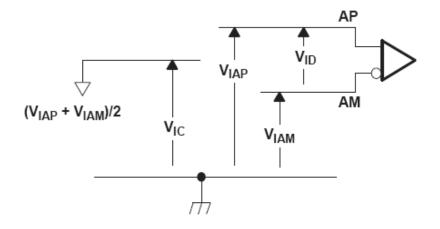
Note 1: The absolute maximum rating values of this product are not allowed to be exceeded any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 2: VR Conditions: Zener Diode 20mA

4. ELECTRICAL CHARACTERISTICS

4-1 TFT LCD Module

		MIN	NOM	MAX	UNIT
V_{DD}	Supply voltage	3	3.3	3.6	٧
V_{IH}	High-level input voltage (SHTDN)	2			٧
V_{IL}	Low-level input voltage (SHTDN)			0.8	٧
$ V_{ID} $	Magnitude differential input voltage	0.1		0.6	٧
V _{IC}	Common-mode input voltage	V _{ID} 2		$2.4-\frac{ V_{\hbox{\scriptsize ID}} }{2}$	V



Item	Symbol	Values			Unit	Remark	
ICIII	Symbol	MIN	TYP	MAX	Offic	Remaik	
LED Driver Power Voltage	V_{LED}	4.5	5.0	5.5	V		
LED Driver Current Consumption	I _{LED}		410		mA	VLED=5V VADJ=3.3V (duty 100%)	

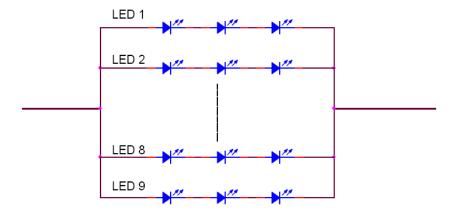
4-2 Backlight Driving Conditions

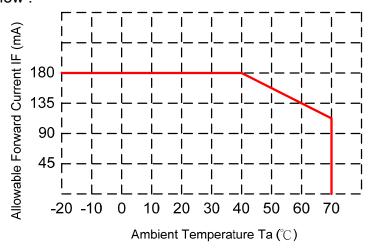
Date: 2013/05/13

Itam	Symbol	Values			Unit	Note
Item	Symbol	Min.	Тур.	Max.	Offic	Note
LED voltage	VL	-	9.9	10.5	V	Note 1
LED current	IL	162	180	198	mA	Ta=25°ℂ
LED life time		20,000			Hr	Note 2

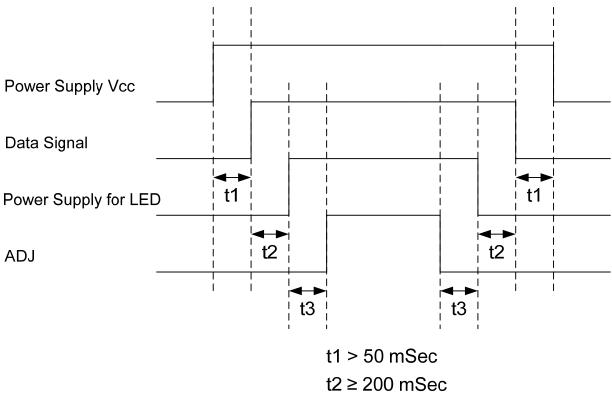
Note 1 : The LED Supply Voltage is defined by the number of LED at Ta=25 $^{\circ}$ C and IL=180mA.

Note 2 : The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL=180mA. The LED lifetime could be decreased if operating IL is larger than 180mA.





4-3 Power Sequence

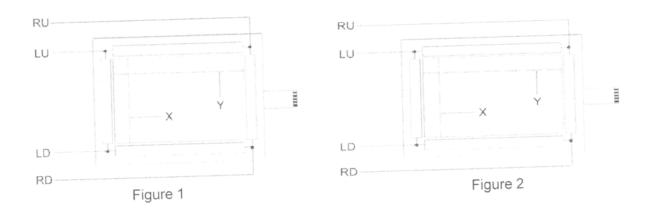


t3 ≥ 50 mSec

Note: Data Signal includes DCLK, DE, HS, VS, R0~R5, G0~G5, B0~B5.

Touch Panel Electrical Specification

Item	Specifications
(1) Supply voltage	DC 7V max.
(2) Circuit Resistance	(1)X:200~900 ohm, Y:200~900 (See Figure 1) 4:3 (2)X:350~1300ohm, Y:120~450ohm(See Figure 2)16:9
(3) Linearity	X ≤1.5%, Y ≤1.5%
(4) Response	≦10ms
(5) Insulation Resistance	10Mohm min/25(DC)
(6) Endurance	No acting damage at DC50V/60sec.



Note:Circuit Resistance X = short RU and RD, short LU and LD, measure the resistance between RU and LU.

Circuit Resistance Y = short RU and LU, short RD and LD, measure the resistance between RU and RD.

Interface

No.	Symbol	Function
1	YU	Touch Panel Top Signal in Y Axis
2	XR	Touch Panel Right Signal in X Axis
3	YD	Touch Panel Bottom Signal in Y Axis
4	XL	Touch Panel Left Signal in X Axis

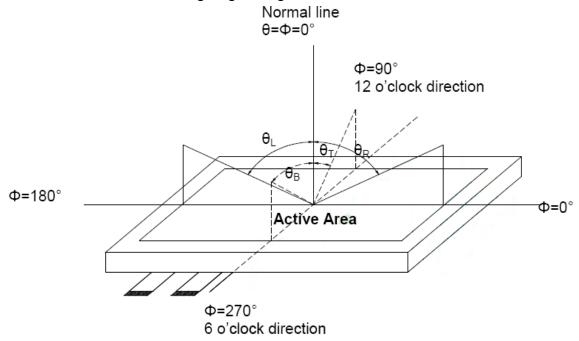
5. Optical Specifications

tam.				Values		11:4	Note
Item	Symbol Condition		Min.	Тур.	Max.	Unit	Note
	θ L	Φ = 180° (9 o'clock)	60	70			
Viewing angle	θ R	$\Phi = 0^{\circ}$ (3 o'clock)	60	70		4	Nistad
(CR≧10)	θ T	$\Phi = 90^{\circ}$ (12 o'clock)	40	50		degree	Note1
	θ B	Φ = 270° (6 o'clock)	60	70			
Dognongo timo	TON			10	20	msec	Noto2
Response time	TOFF			15	30	msec	Note3
Contrast ratio	CR		400	500			Note4
Color	WX	Normal θ =Φ=0°	0.26	0.31	0.36		Note5
chromaticity	WY		0.28	0.33	0.38		Note6
Luminance	L		160	200		cd/m²	Note6
Luminance uniformity	YU		70	75		%	Note7

Test Conditions:

- 1. VCC = 3.3V, I_L = 180mA (Backlight current), the ambient temperature is $25^{\circ}C$.
- 2. The test systems refer to Note 2.

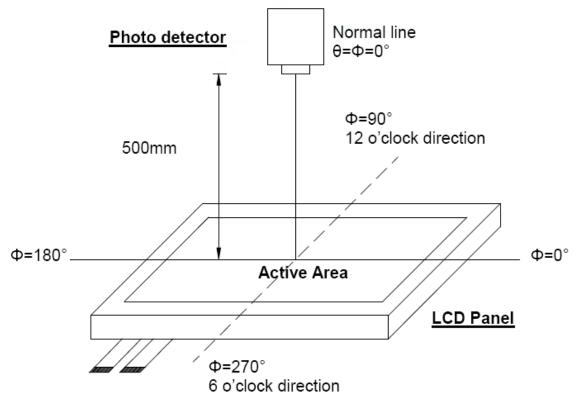
Note 1: Definition of viewing angle range



Note 2: Definition of optical measurement system.

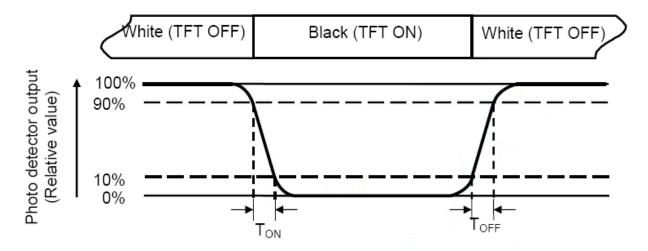
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The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° / Height: 500mm.)



Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (Ton) is the time between photo detector output intensity changed from 90% to 10%. And fall time (Toff) is the time between photo detector output intensity changed from 10% to 90%.



Note 4: Definition of contrast ratio

Contrast ratio (CR) =

Luminance measured when LCD on the "White" state

Luminance measured when LCD on the "Black" state

Note 5 : Definition of color chromaticity (CIE1931)

Color coordinated measured at center point of LCD.

Note 6 : All input terminals LCD panel must be ground when measuring the center area of the panel.

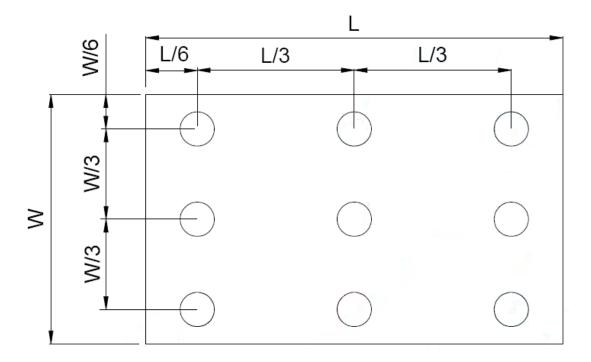
Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to bellow figure). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (Yu) = _____

Bmax

L ----- Active area length W ----- Active area width



B_{max}: The measured maximum luminance of all measurement position. B_{min}: The measured minimum luminance of all measurement position.

6. INTERFACE

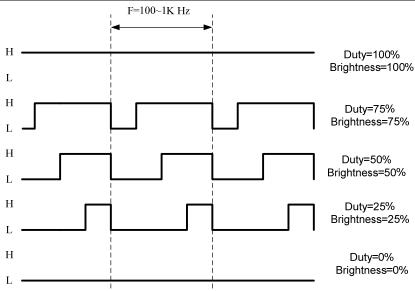
TFT LCD Panel Driving Section

Pin No.	Symbol	Description	Note
1	VDD	POWER SUPPLY:3.3V	
2	VDD	POWER SUPPLY:3.3V	
3	GND	Power Ground	
4	GND	Power Ground	
5	INO-	Transmission Data	
6	IN0+	Transmission Data	
7	GND	Power Ground	
8	IN1-	Transmission Data	
9	IN1+	Transmission Data	
10	GND	Power Ground	
11	IN2-	Transmission Data	
12	IN2+	Transmission Data	
13	GND	Power Ground	
14	CLK-	Sampling Clock	
15	CLK+	Sampling Clock	
16	GND	Power Ground	
17	ADJ	Adjust the LED brightness	(1)
18	NC	No connection.	
19	GND/VLED	JP7=1-2 & JP5=2-3short → power supply of LED driver circuit.	(0)
20	GND/VLED	JP7=2-3 & JP5=1-2short →GND terminal. (default setting)	(2)

NOTE:

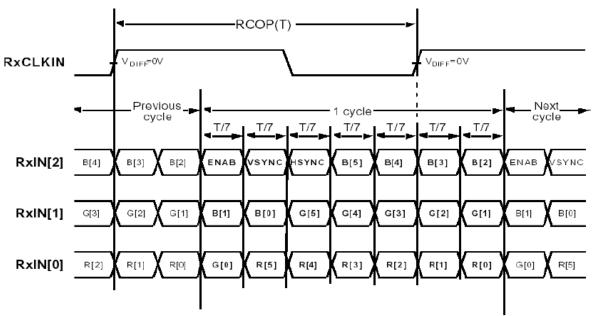
(1) Pin3: ADJ is PWM signal input. It is for brightness control.

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
ADJ signal frequency	fрwм	100		1K	Hz
ADJ signal logic level High	VIH	2V	1	VLED (5.0V)	٧
ADJ signal logic level Low	VIL	0	ı	0.5	V



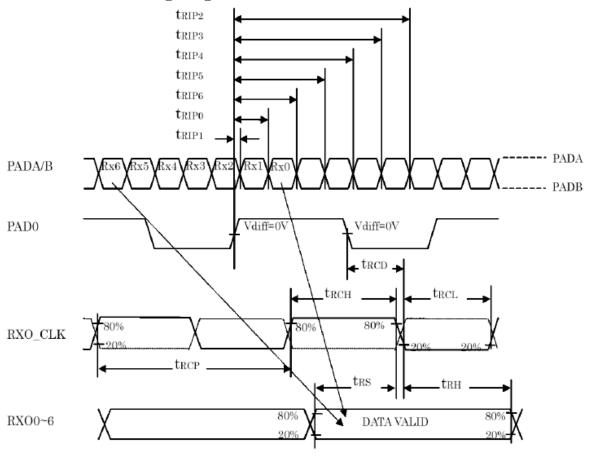
(2)

7. INPUT SIGNAL: 7-1 LVDS SIGNAL:

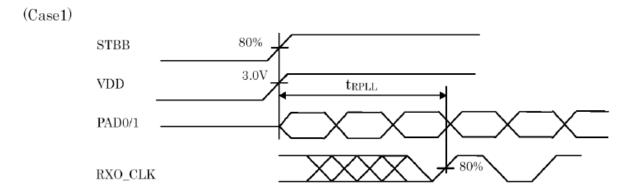


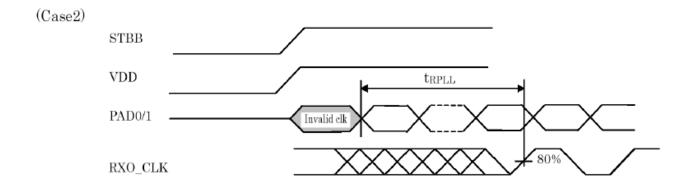
Note: R/G/B[5]s are MSBs and R/G/B[0]s are LSBs

7-2 LVDS AC Timing Diagram:



7-3 Phase Lock Loop Set Time :





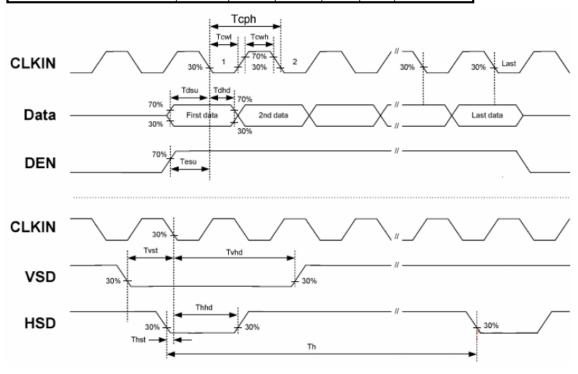
7-3 AC Electrical Characteristics

ltem	Cumbal	Values			Unit	Remark
item	Symbol	Min.	Тур.	Max.	Unit	Remark
HS setup time	Thst	8	-	-	Ns	
HS hold time	Thhd	8	-	-	Ns	
VS setup time	Tvst	8	-	-	Ns	
VS hold time	Tvhd	8	-	-	Ns	
Data setup time	Tdsu	8	-	-	Ns	
Data hole time	Tdhd	8	-	-	Ns	
DE setup time	Tesu	8	-	-	Ns	
DE hole time	Tehd	8	-	-	Ns	
VDD Power On Slew rate	Tpor	-	-	20	ms	
RSTB pulse width	TRst	10	-	-	us	
CLKIN cycle time	Tcoh	20	-	-	Ns	
CLKIN pulse duty	Tewh	40	50	60	%	
Output stable time	Tsst	-	-	6	us	

7-4 Recommended Input Timing of LVDS transmitter

	-					
ltem	Symbol	Values			Unit	Remark
item		Min.	Тур.	Max.	Onit	Kemark
Horizontal Display Area	thd	-	800	-	DCLK	
DCLK Frequency	fclk	-	40	50	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	
HS pulse width	thpw	1	-	40	DCLK	
HS Back Porch(Blanking)	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

Item	Symbol	Values			Unit	Remark
item		Min.	Тур.	Max.	Unit	Remark
Vertical Display Area	tvd	-	600	-	TH	
VS period time	tv	624	635	700	TH	
VS pulse width	tvpw	1	-	20	TH	
VS Back Porch(Blanking)	tvb	23	23	23	TH	
VS Front Porch	tvfp	1	12	77	TH	



8. Reliability Test Items

Date: 2013/05/13

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	40°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 70°C (30min) 20 cycles	1,2
Vibration Test (Packing)	Sweep frequency: 10 ~ 55 ~ 10 Hz/1min Amplitude: 0.75mm Test direction: X.Y.Z/3 axis Duration: 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

9. General Precautions

9-1 Safety

Liquid crystal is poisonous. Do not put it your month. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

9-2 Handling

- 1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- 2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
- 3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
- 4. Keep a space so that the LCD panels do not touch other components.
- 5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
- 6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
- 7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

9-3 Static Electricity

- 1. Be sure to ground module before turning on power or operation module.
- 2. Do not apply voltage which exceeds the absolute maximum rating value.

9-4 Storage

- 1. Store the module in a dark room where must keep at +25±10℃ and 65%RH or less.
- 2. Do not store the module in surroundings containing organic solvent or corrosive gas.
- 3. Store the module in an anti-electrostatic container or bag.

9-5 Cleaning

- 1. Do not wipe the polarizer with dry cloth. It might cause scratch.
- 2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

9-5 Others

- 1. AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.
- The residual image may exist if the same display pattern is shown for hours. This residual image, however, disappears when another display pattern is shown or the drive is interrupted and left for a while. But this is not a problem on reliability.

10. OUTLINE DIMENSION

